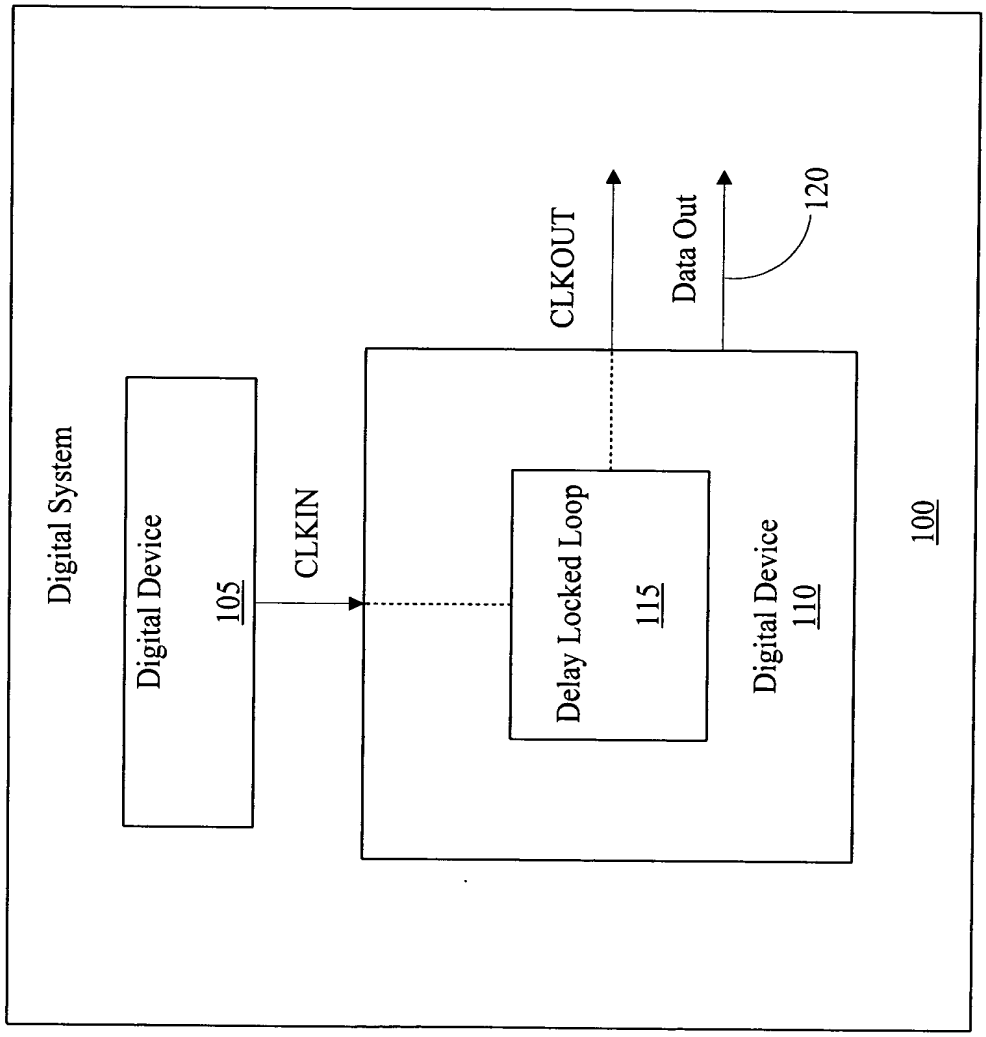
Digital System
Digital Device
105
CLKIN
Delay Locked Loop
115
Digital Device
110
CLKOUT
Data Out
120
100

Figure 1

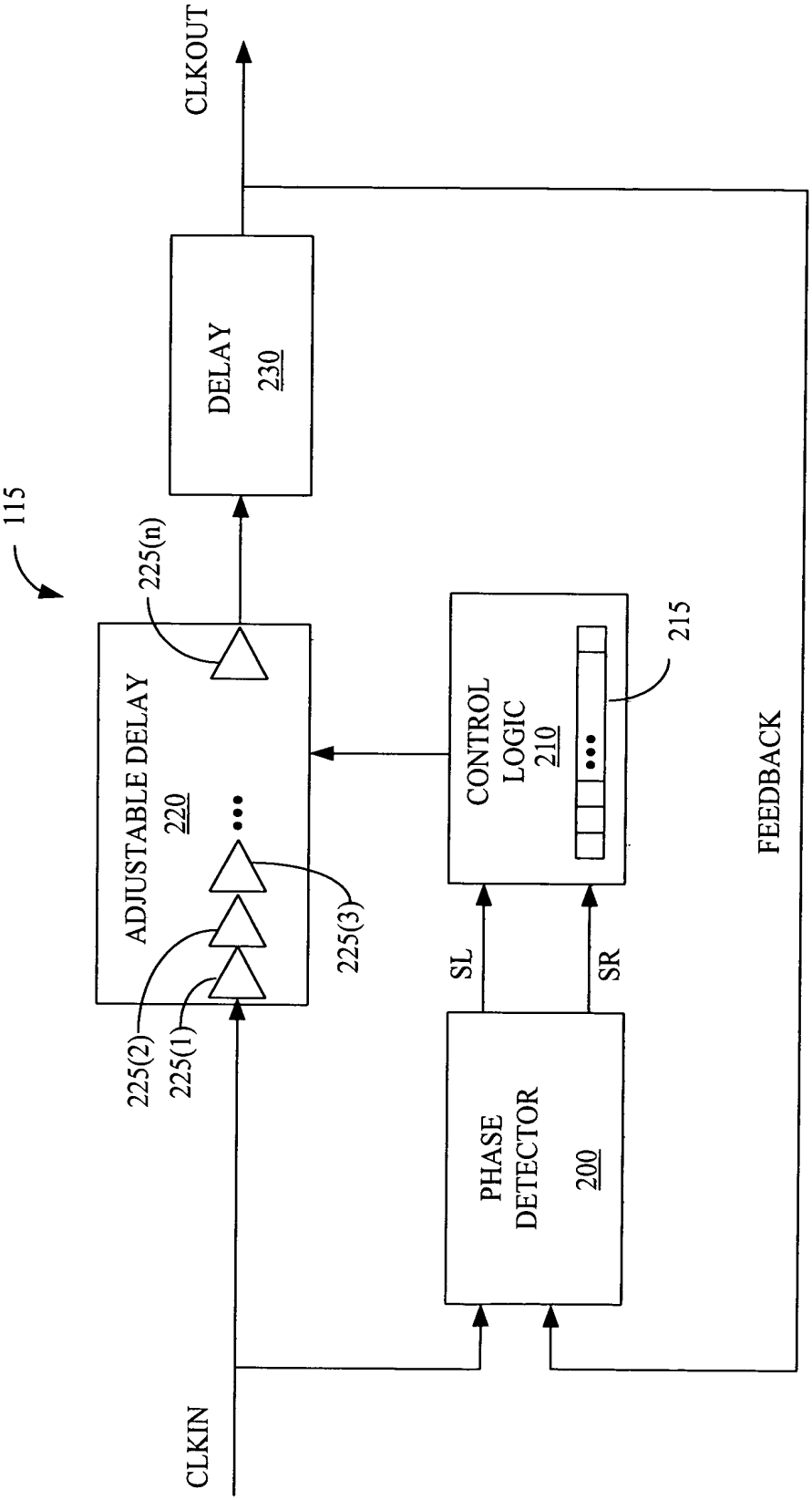
CLKOUT
DELAY
230
115
225(n)
ADJUSTABLE DELAY
220
225(3)
225(2)
225(1)
CONTROL
LOGIC
210
215
SL
SR
PHASE
DETECTOR
200
FEEDBACK
CLKIN

Figure 2

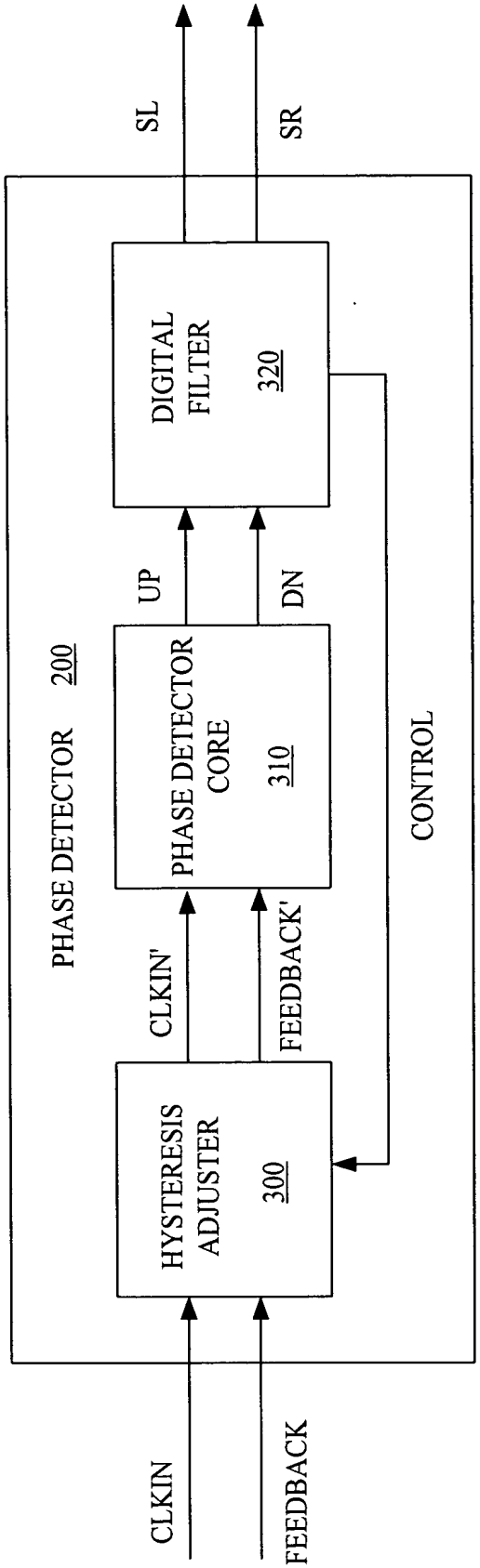

Figure 3

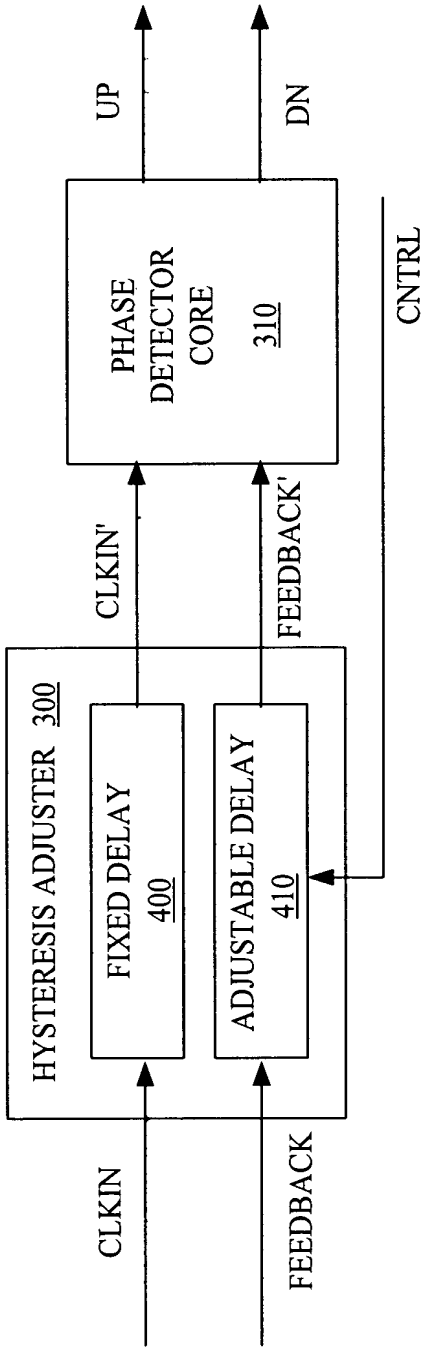

Figure 4

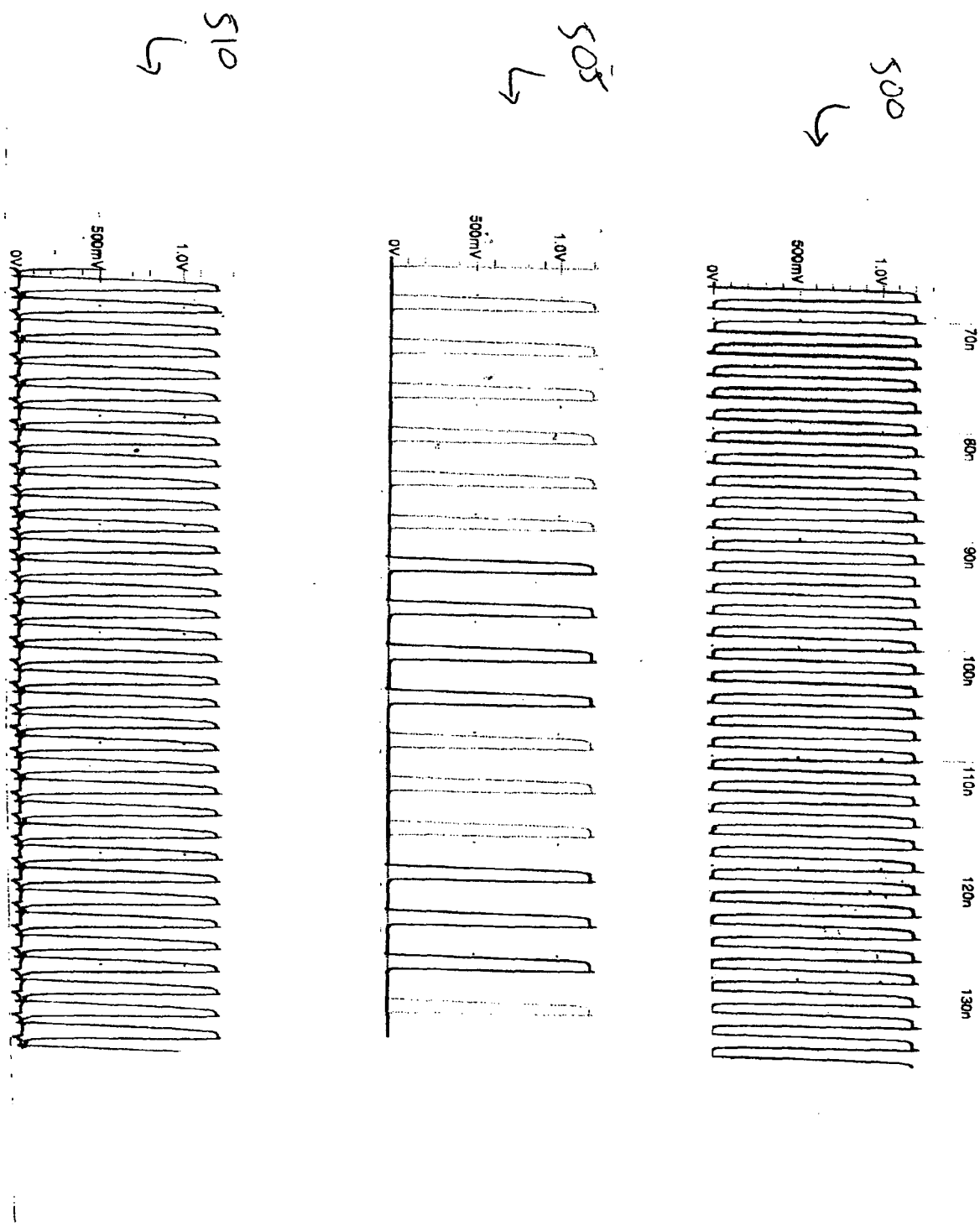
500
1.0V
500mV
0V
70n
80n
90n
100n
110n
120n
130n
505
1.0V
500mV
0V
510
1.0V
500mV
0V

Figure 5A

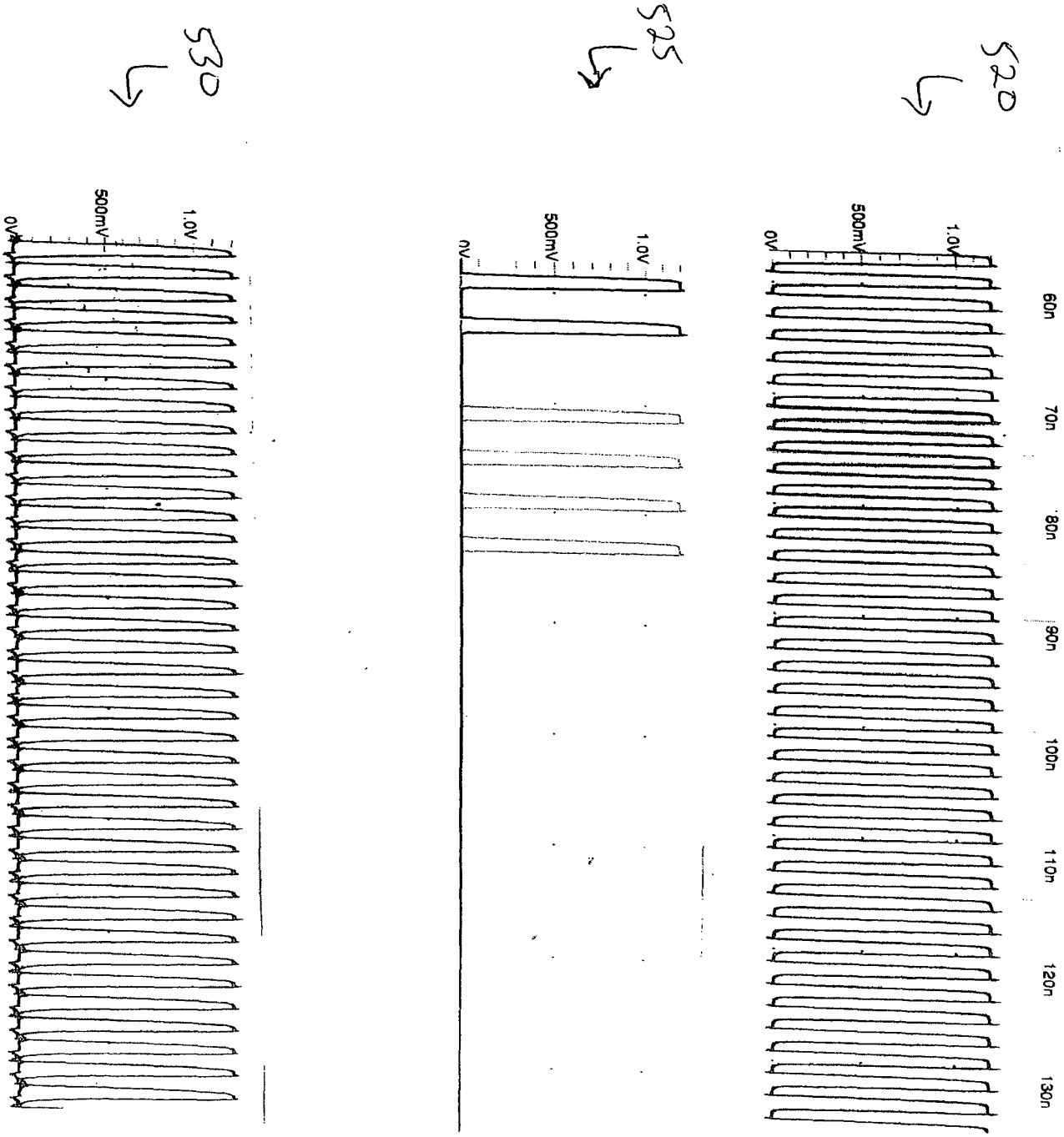

Figure 5B